Electronic Acknowledgement Receipt			
EFS ID:	1297193		
Application Number:	10085046		
International Application Number:			
Confirmation Number:	7805		
Title of Invention:	Process for production of semiconductor substrate		
First Named Inventor/Applicant Name:	Kiyofumi Sakaguchi		
Customer Number:	5514		
Filer:	Lawrence A. Stahl/Talaya Myers		
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File Listing:

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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	FifthIDS035000105305USA6 00.pdf	185771	no	4
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2	Foreign Reference	EP630035000105305USA60 0.pdf	3333278	no	47
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3	Foreign Reference	JP955035000105305USA60 0.pdf	181467	no	5
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4	NPL Documents	YasumatsuArticle035000105 305USA600.pdf	377487	no	8
Warnings:					
Information:					
5	NPL Documents	HandbookThinFilm03500010 5305USA600.pdf	2527792	no	16
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6	NPL Documents	PerretArticle035000105305U SA600.pdf	1275207	no	25
Warnings:					
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7	NPL Documents	PhysicsSemiconductorDevic es035000105305USA600.pd f	3436242	no	43
Warnings:		<u> </u>			
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9	NPL Documents	LuftTsuoArticle03500010530 5USA600.pdf	1371739	no	13
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/IDO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filling Receipt, in due course.